

FIG. 1

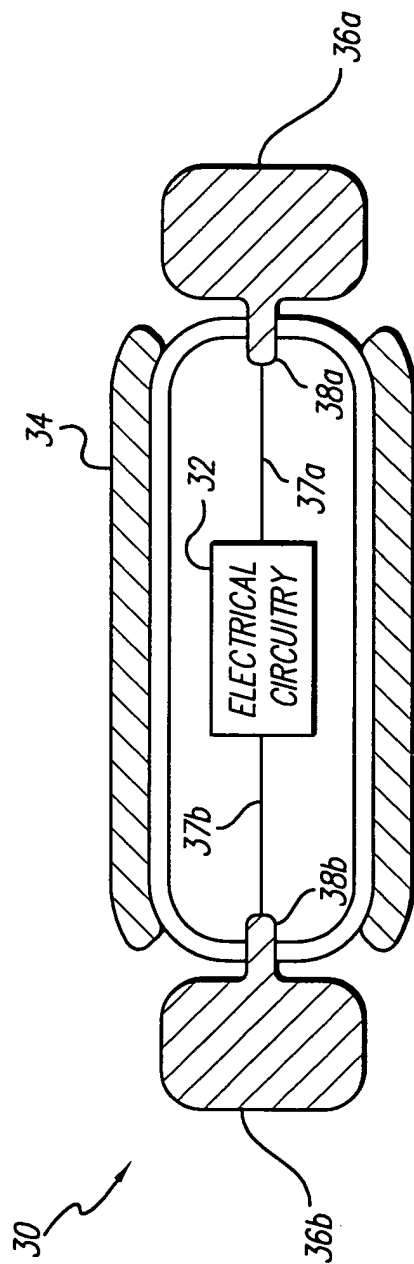


FIG. 2

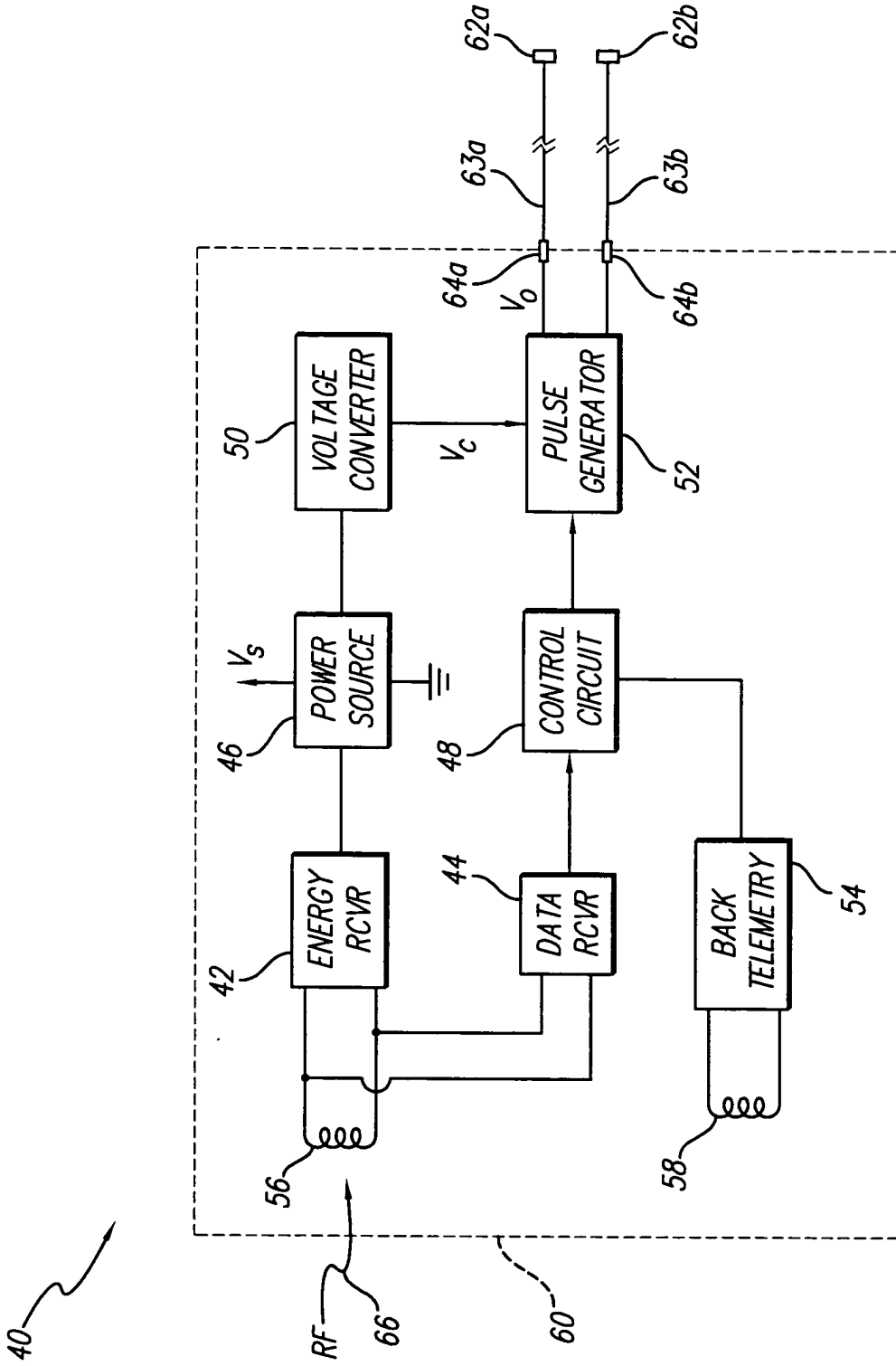


FIG. 3

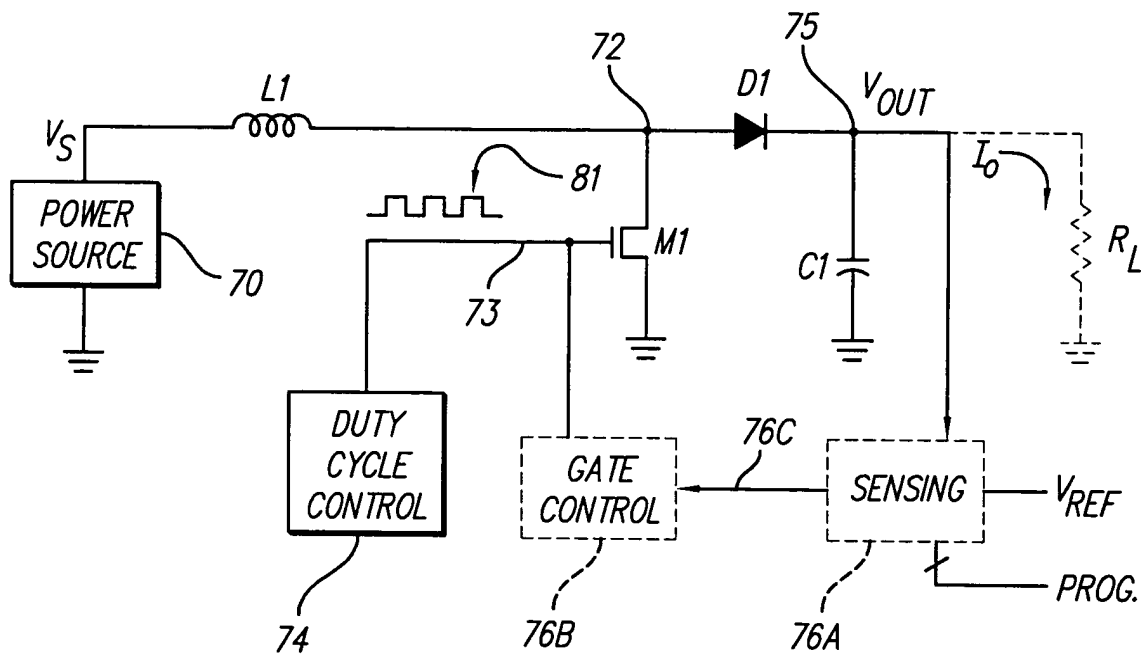
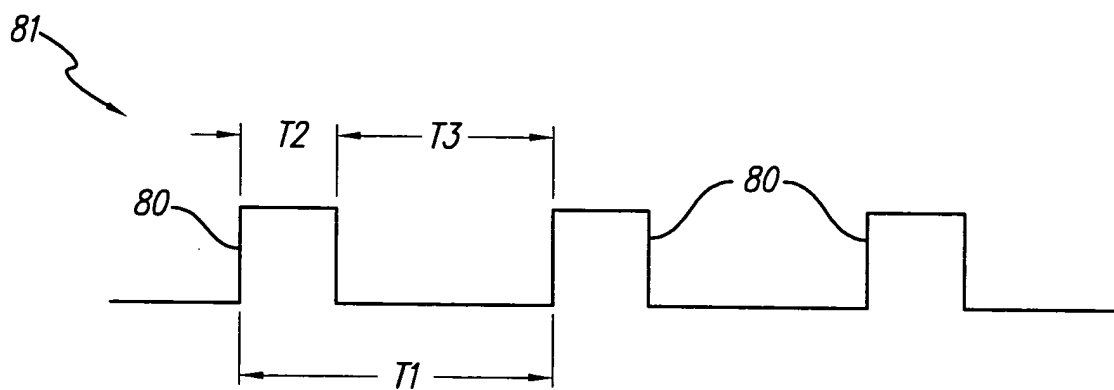
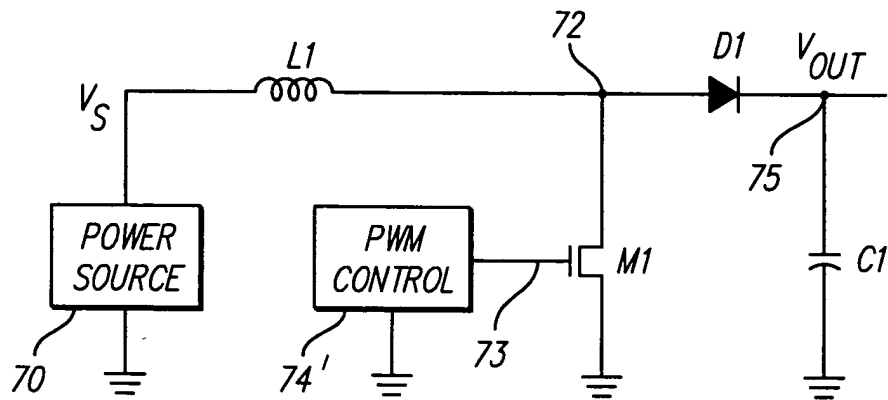
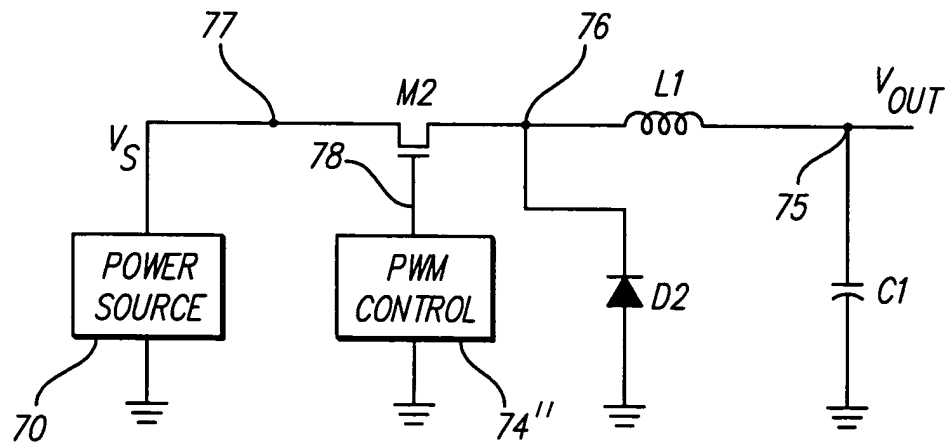


FIG. 4



$$T2/T1 = \text{DUTY CYCLE}$$

FIG. 5

**FIG. 6A****FIG 6B**

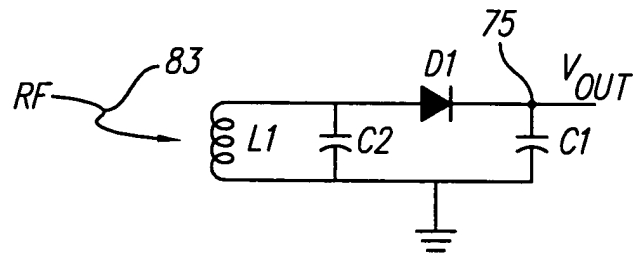


FIG. 6C

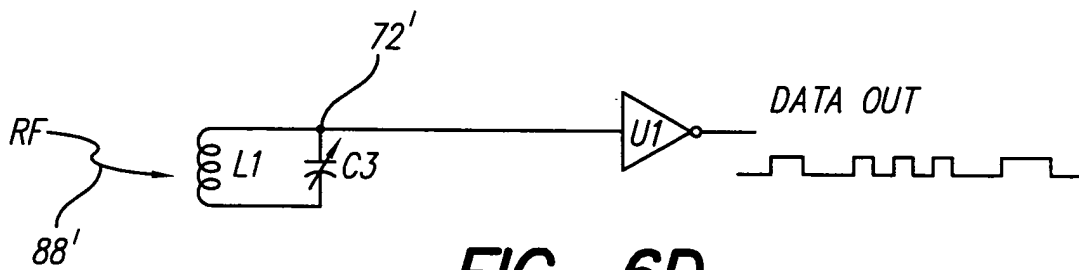


FIG. 6D

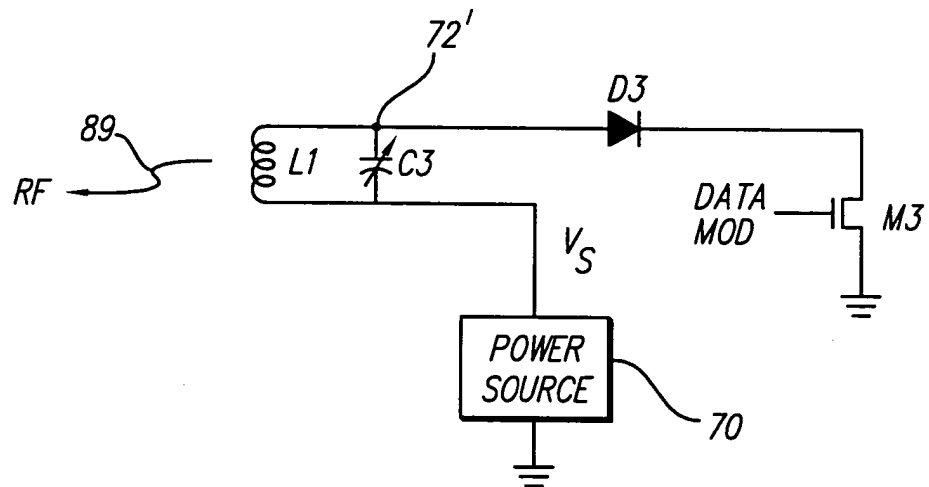


FIG. 6E



**FIG. 7**

<i>MODE</i>	<i>M1'</i>	<i>M2</i>	<i>M3</i>	<i>M4</i>	<i>M5</i>
<i>STEP UP</i>	<i>ON</i>	<i>OFF</i>	<i>PWM ON</i>	<i>OFF</i>	<i>OFF</i>
<i>STEP DOWN</i>	<i>OFF</i>	<i>PWM ON</i>	<i>OFF</i>	<i>OFF</i>	<i>OFF</i>
<i>ENERGY RECEIVE</i>	<i>OFF</i>	<i>OFF</i>	<i>OFF</i>	<i>ON</i>	<i>ON</i>
<i>DATA RECEIVE</i>	<i>OFF</i>	<i>OFF</i>	<i>OFF</i>	<i>ON</i>	<i>ON</i>
<i>DATA XMIT</i>	<i>ON</i>	<i>OFF</i>	<i>MODULATION ON</i>	<i>ON</i>	<i>ON</i>

*FIG. 8*